SSI2190



PROCIRCUITTM 6-INTO-1 VOLTAGE CONTROLLED MIXER*

The SSI2190 is a six-into-one voltage controlled mixer in a compact 24-lead SSOP package, based on a new-generation Operational Transconductance Amplifier (OTA) developed by Sound Semiconductor. The high-compliance current output allows easy paralleling of multiple SSI2190s.

Each input channel has differential voltage inputs and a current-mode linear control input. This combination results in low distortion and control feedthrough, along with wide dynamic range.

The SSI2190 makes mixing of audio signals – as well as control voltages – a simple endeavor. Differential inputs can be used for phase correction, differential signal paths, as well as countless other applications that are only limited to one's imagination. A minimum of inexpensive external components are required for operation.

A wide supply voltage range (single or dual) allows use in a variety of audio gear from musical instruments and effects pedals to prosumer systems where large signal handling and headroom are desired.

FEATURES

- Easy-to-Use Six Input into Single Output Audio Mixer
- Handles Input Signals up to 10V_{RMS}
- Linear Control OTA's
- Very Low Noise: Typical –100dBu
- Low Distortion Typical 0.02%
- Mute Attenuation of -xxxdB
- ±4V to ±18V Operation
- Very Few External Components Required
- Low Control Feedthrough Typical -xxdB



PIN CONNECTIONS 24-LEAD SSOP



FUNCTIONAL BLOCK DIAGRAM

*Patent Pending



Parameter	Symbol	Conditions	Min	Тур	Max	Units		
POWER SUPPLY								
Supply Voltage Range	Vs		±4		±18	V		
Supply Current - Positive	I _{SY+}	All six channels active		+11.6	+xx	mA		
Supply Current - Negative	I _{SY-}	All six channels active		-12.3	-xx	mA		
Power Supply Rejection Ratio	PSRR	60Hz		65		dB		
CONTROL PORTS								
Control Current Range	ICTRL	At CTRL pins, mute to full on	0		100	μA		
Transconductance	g _m	After 60 seconds	хх	8350	xx	μS		
<i>g</i> _m Tracking				XX		dB		
Control Feedthrough				ХХ		dB		
Maximum Attention*		$I_{CTRL} = 0\mu A; V_{CTRLREF} = -xxmV$		ХХ		dB		
SIGNAL INPUTS								
Maximum Input Voltage		At IN+ and IN– pins	V– +2V		V+ –2V	V		
Maximum Differential Input Voltage		Between any IN+/IN- pair			±1	V		
Input Resistance				ХХ		kΩ		
Input Bias Current	IB			2.0		μA		
Input Offset Current	los			20		nA		
Common Mode Rejection	CMRR			68		dB		
SIGNAL OUTPUTS								
Output Compliance			V– +2V		V+ –2V	V		
Output Offset Current		V _{IN} = GND		ХХ		nA		
Max Recommended Output Current		THD = 1%			XX	mA		
PERFORMANCE								
Output Noise		V _{IN} = GND		-100		dBu		
Headroom	HR	@1% THD		xx		dBu		
Total Harmonic Distortion	THD			0.02		%		
Channel Separation		??		ХХ		dB		
Slew Rate	SR			XX		μA/μs		

*see Control Reference section for further information

ABSOLUTE MAXIMUM RATINGS

Supply Voltage	±20V	
Maximum Control Current	1mA	
Maximum Differential Input Voltage	±4V	
Storage Temperature Range	-65°C to +150°C	
Operating Temperature Range	-40°C to +85°C	
Lead Temperature (Soldering 10 sec)	260°C	

ORDERING INFORMATION

Part Number	Package Type/Container	Quantity	
SSI2190SS-TU	24-Lead SSOP* - Tube	58	
SSI2190SS-RT	24-Lead SSOP* - Tape and Reel	4000	

*SSI Package ID "**PSSL24**", compliant with JEDEC MO-137-AE Mechanical drawing available at www.soundsemiconductor.com

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PIN DESCRIPTIONS ("x" refers to one of the six channels)

Pin(s)	Name	Description
1	V–	Negative supply. Recommend 100nF local decoupling capac- itor placed as close to package as possible with a low induc- tance trace to ground.
2, 3, 4, 8, 9, 10	IN x+	Non-Inverting voltage signal input
5, 6, 7, 18, 19, 20	CTRL x	Control current input referenced to CTRL REF
11, 14	NC	Leave these pins unconnected
12	MIX OUT	High-compliance current output
13	CTRL REF	Common reference for the control inputs. In a bipolar power supply system connect to control ground; if single supply to a pseudo ground. See Control Reference for more information about use of this pin.
15, 16, 17, 21, 22, 23	IN x–	Inverting voltage signal input. Differential input should not exceed ±100mV.
24	V+	Positive supply. Recommend 100nF local decoupling capacitor placed as close to package as possible with a low inductance trace to ground.



Figure 1: Typical Application Circuit